



L7: (12) 6 and mul
 L8: (164) 4 and fi
 L9: (161) 4 and se
 L10: (170) 8 or 9
 L11: (23) 10 and mu
 L12: (23) 11 and th
 L13: (9) 12 and com

☒ Failed

 USPAT;US-PGPUB;EPO;JPO;ISM;TDB

☒ Burels

 Default operator:
☒ Highlight all hit terms initially.

☒ BRS term ☒ SAR term ☐ Image ☒ Text ☐ HTML

	U	I	PT	P	Document ID	Issue Dat	Pages	Title	Current/OR	Current.XR	Retrieval	Inventor	S	C	3	
1	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20020141259	20021003	12	Memory cell structural test	365/201			Mak, Tak M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6502145 B1	20021231	28	Semiconductor memory with application of pre	710/22	365/145; 365/203;		Ooishi, Tsukasa	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6208575 B1	20010327	109	Dynamic memory array bit line sense amplifie	365/208	257/E27.09 7;		Proebsting, Robert J.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6038192 A	20000314	71	Memory cells for field programmable memory arr	365/230.03	365/189.08		Clinton, Kim P. N. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5986913 A	19991116	15	Storage circuitry using sense amplifier shared	365/51	365/205; 365/230.08		Childers, Jimmie Don et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5737569 A	19980407	24	Multiport high speed memory having contentio	711/149	711/150; 711/151;		Nadir, James et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5579273 A	19961126	16	Storage circuitry using sense amplifier shared	365/205	365/190; 365/208;		Childers, Jimmie D. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5414824 A	19950509	25	Apparatus and method for accessing a split l	711/3	365/49		Grochowski, Edward T.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5289428 A	19940222	44	Semiconductor memory device	365/230.05	365/230.03		Sato, Katsuyuki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

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